

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 09/11/03
Application Type:: Regular
Subject Matter:: Utility
Title:: Leakage Power Optimization For Integrated Circuits
Attorney Docket Number:: X-1396 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 5
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Canada
Status:: Full Capacity
Given Name:: Jason
Middle Name:: H.
Family Name:: Anderson
Street:: 11 Joseph Salsberg Lane
City:: Toronto
State or Province:: Ontario
Country:: Canada
Postal or Zip Code:: M6J-3W8

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Farid
Middle Name:: N.
Family Name:: Najm
Street:: 3126 Workman Drive

City:: Mississauga
State or Province:: Ontario
Country:: Canada
Postal or Zip Code:: L5M-6K5

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124